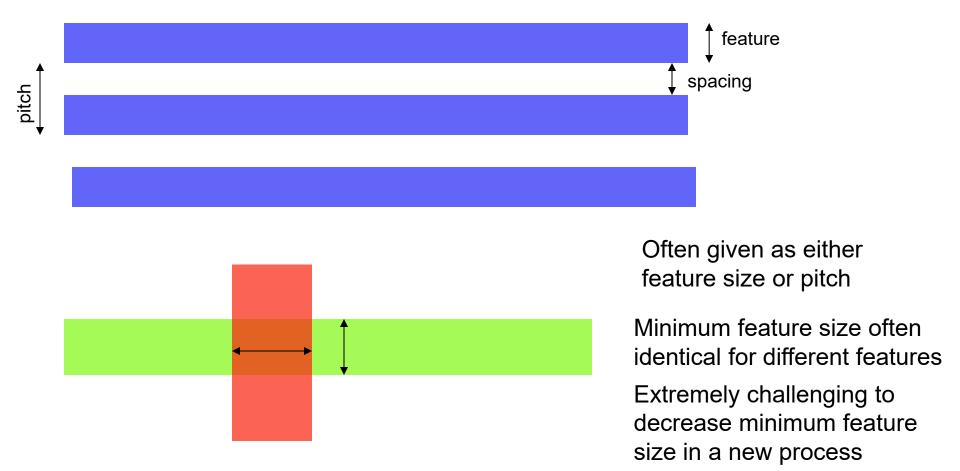
EE 330 Lecture 4

- Yield
- Statistics Review

Feature Size

Feature size is the minimum lateral feature size that can be **reliably** manufactured



What is meant by "reliably"

Yield is acceptable if circuit performs as designed even when a very large number of these features are made

If P is the probability that a feature is good

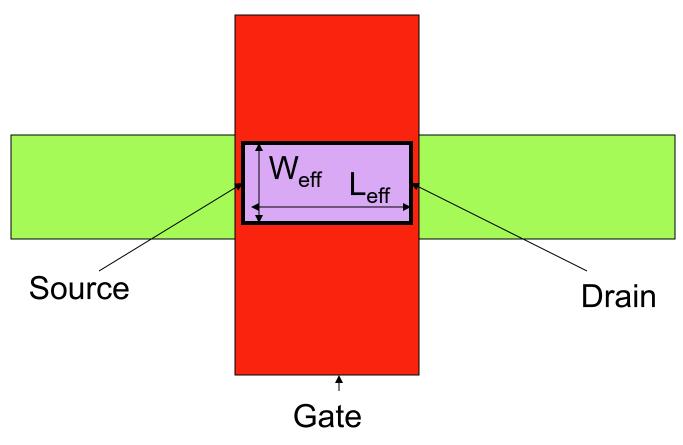
n is the number of uncorrelated features on an IC

Y is the yield

$$Y = P^{n}$$

$$P = e^{\frac{\log_{e} Y}{n}}$$

MOS Transistor



Effective Width and Length Generally Smaller than Drawn Width and Length

Air

Physical Characteristics of Key Semiconductor Materials

 \mathbf{O}

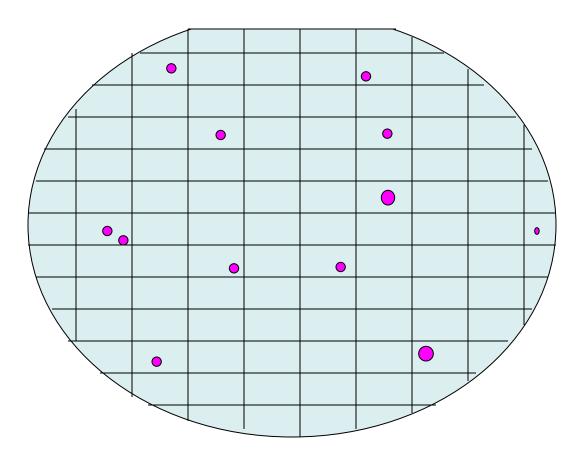
20KV/cm

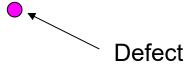
Silicon:	Average Atom Spacing	2.7 Å
	Lattice Constant	5.4 Å
S_iO_2	Average Atom Spacing	3.5 Å
	Breakdown Voltage	5 to 10 MV/cm = 5 to 10 mV/ $\overset{0}{A}$

Physical size of atoms and molecules place fundamental limit on conventional scaling approaches

Review from last lecture:

Defects in a Wafer





- Dust particles and other undesirable processes cause defects
- Defects in manufacturing cause yield loss

Hard Fault Model

$$Y_H = e^{-Ad}$$

Y_H is the probability that the die does not have a hard fault A is the die area d is the defect density (for some older processes, typically 1cm⁻² < d < 2cm⁻²) for some newer processes, typically 0.1cm⁻² < d < 1cm⁻²)

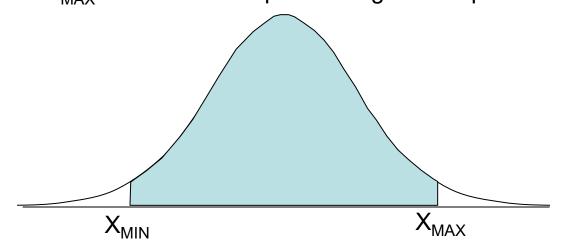
Industry often closely guards the value of d for their process

Other models, which may be better, have the same general functional form

Soft Fault Model

$$P_{\text{SOFT}} = \int_{X_{\text{MIN}}}^{X_{\text{MAX}}} f(x) dx$$

 P_{SOFT} is the soft fault yield f(x) is the probability density function of the parameter of interest X_{MIN} and X_{MAX} define the acceptable range of the parameter of interest



Some circuits may have several parameters that must meet performance requirements

Soft Fault Model

If there are k parameters that must meet parametric performance requirements and if the random variables characterizing these parameters are uncorrelated, then the soft yield is given by

$$Y_S = \prod_{j=1}^k P_{SOFT_j}$$

Overall Yield

If both hard and soft faults affect the yield of a circuit, the overall yield is given by the expression

$$Y = Y_H Y_S$$

Cost Per Good Die

The manufacturing costs per good die is given by

$$C_{Good} = \frac{C_{FabDie}}{Y}$$

where C_{FabDie} is the manufacturing costs of a fab die and Y is the yield

There are other costs that must ultimately be included such as testing costs, engineering costs, packaging costs, etc.

Do you like statistics?

Statistics are Real!

Statistics govern what really happens throughout much of the engineering field!

Statistics are your Friend !!!!

You might as well know what will happen since statistics characterize what WILL happen in the presence of variability in many processes!

Assume x is a random variable of interest

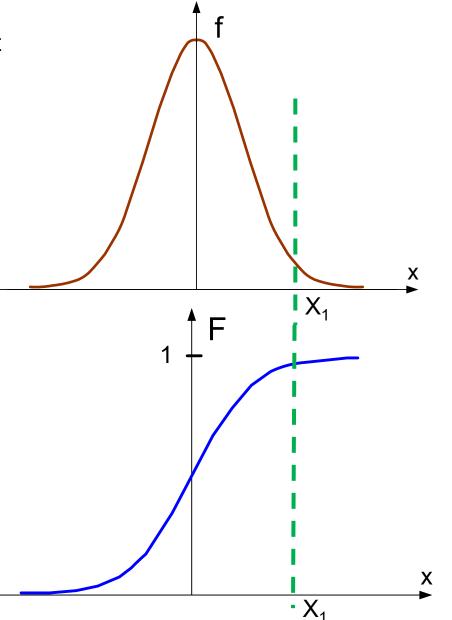
f(x) = Probability Density Function for x

$$\int_{\mathbf{x}=-\infty}^{\infty} \mathbf{f}(\mathbf{x}) \, d\mathbf{x} = 1$$

F(x) = Cumulative Density Function for x

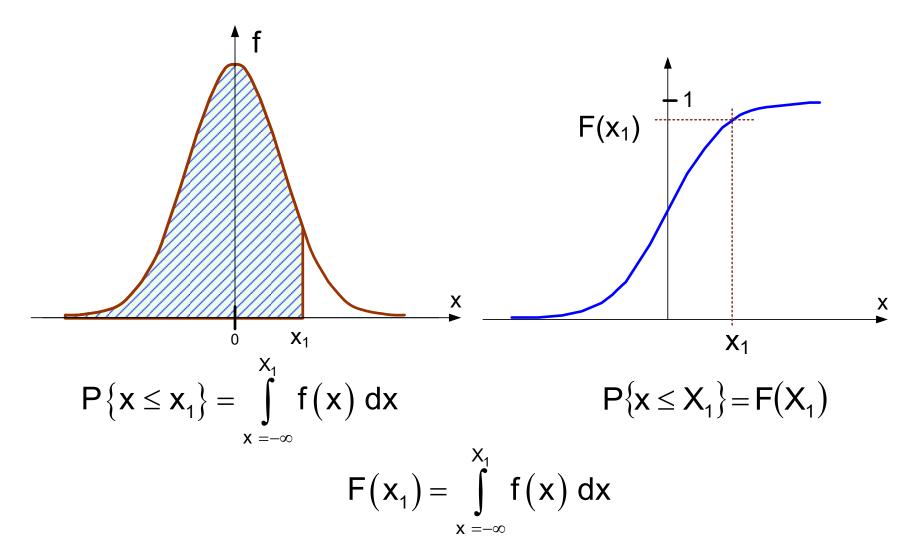
$$F(X_1) = \int_{x=-\infty}^{X_1} f(x) dx$$

$$0 \le F(x) \le 1$$
 $\frac{\partial F(x)}{\partial x} \ge 0$

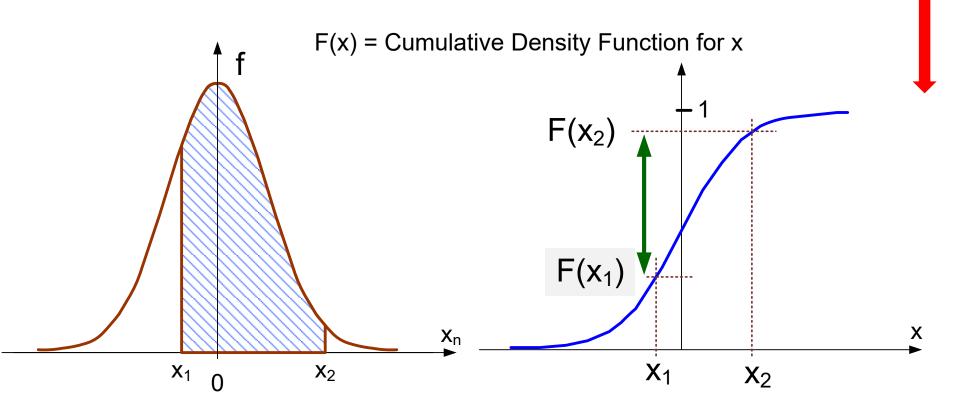


f(x) = Probability Density Function for x

F(x) = Cumulative Density Function for x

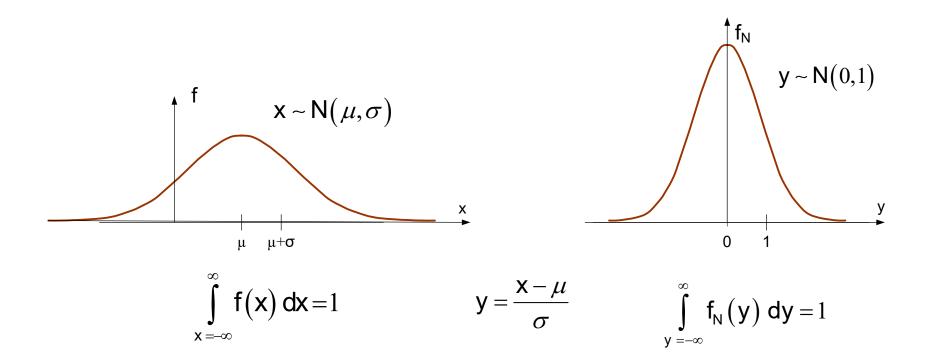


f(x) = Probability Density Function for x



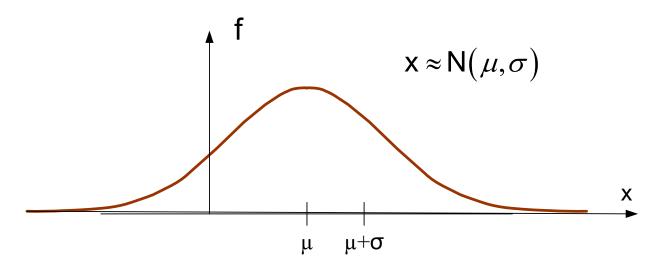
$$P\{X_1 \le x \le X_2\} = \int_{X_1}^{X_2} f(x) dx$$

$$P{X_1 \le x \le X_2} = F(X_2) - F(X_1)$$



Theorem 1: If the random variable x is normally distributed with mean μ and standard deviation σ , then $y = \frac{x - \mu}{\sigma}$ is also a random variable that is normally distributed with mean 0 and standard deviation of 1.

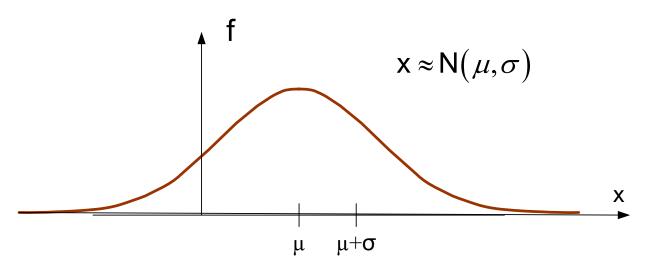
(Normal Distribution and Gaussian Distribution are the same)



The random part of many parameters of microelectronic circuits is often assumed to be Normally distributed and experimental observations confirm that this assumption provides close agreement between theoretical and experimental results

The mapping $y = \frac{x - \mu}{\sigma}$ is often used to simplify the statistical characterization of the random parameters in microelectronic circuits

x generally is dimensioned, y is dimensionless

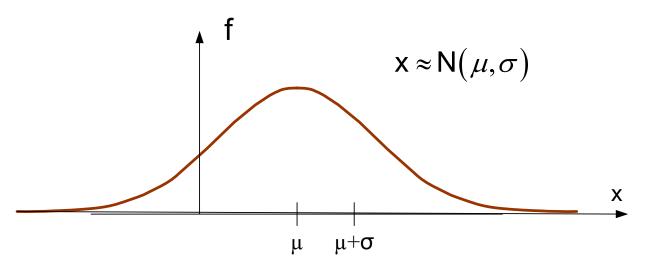


Example:

x might be the frequency of oscillation of a ring oscillator used for a clock in a crystal-less digital circuit, x Gaussian (Normal)

Dimensions of x : Hz Maybe μ =550 MHz σ =50 MHz

$$y = \frac{x - \mu}{\sigma}$$
 is dimensionless with $\mu_y = 0$ $\sigma_y = 1$



Example:

x might be the offset voltage of an op amp, x Gaussian (Normal)

Dimensions of x : Volts

Typically μ =0V σ =10 mV

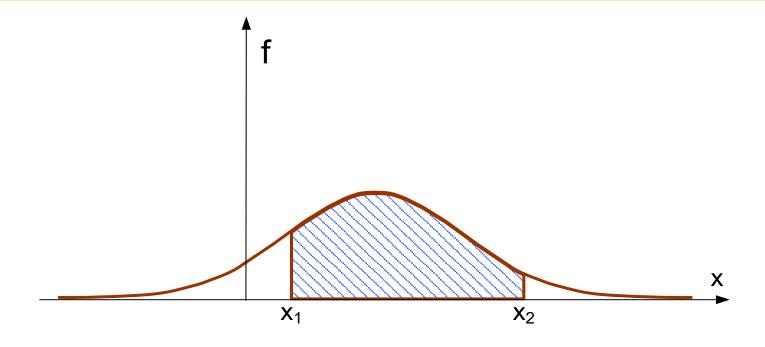
$$y = \frac{x - \mu}{\sigma}$$
 is dimensionless with $\mu_y = 0$ $\sigma_y = 1$

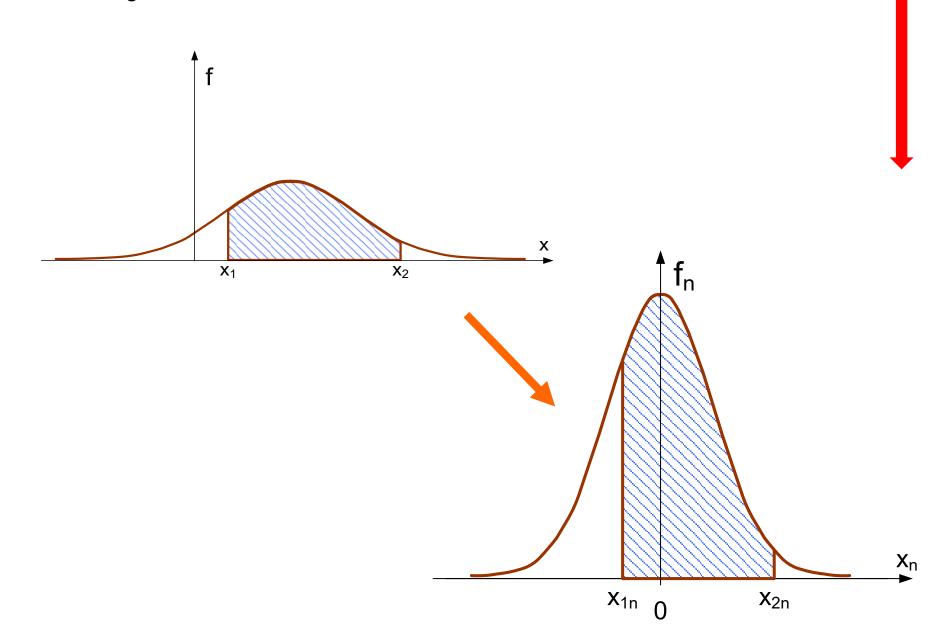
y:
$$N(0,1)$$

Theorem 2: If x is a Normal (Gaussian) random variable with mean μ and standard deviation σ , then the probability that x is between x_1 and x_2 is given by

$$p = \int_{x_1}^{x_2} f(x) dx = \int_{x_{1n}}^{x_{2n}} f_n(x) dx \quad \text{where} \quad x_{1n} = \frac{x_1 - \mu}{\sigma} \quad \text{and} \quad x_{2n} = \frac{x_2 - \mu}{\sigma}$$

and where $f_n(x)$ is N(0,1)

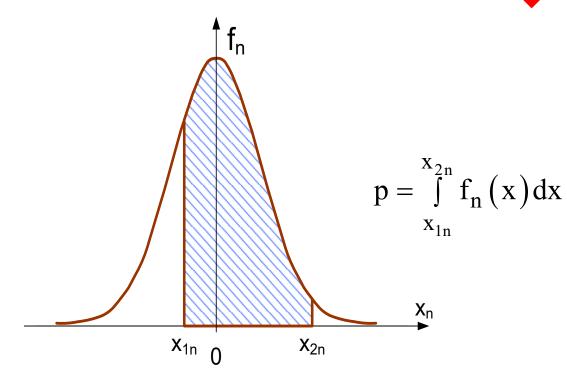




Observation: The probability that the N(0,1) random variable x_n satisfies the relationship $x_{1n} < x_n < x_{2n}$ is also given by

$$p = F_n(x_{2n}) - F_n(x_{1n})$$

where $F_n(x)$ is the CDF of x_n .



Since the N(0,1) distribution is symmetric around 0, p can also be expressed as

$$p = F_n(x_{2n}) - (1 - F_n(-x_{1n}))$$

Observation: In many electronic circuits, a random variable of interest, x, is 0 mean Gaussian and the probabilities of interest are characterized by a region defined by the <u>magnitude</u> of the random variable (i.e. $-x_1 < x < x_1$).

In these cases, if we define $x_N = \frac{x-0}{\sigma}$ then x_N is N(0,1) and

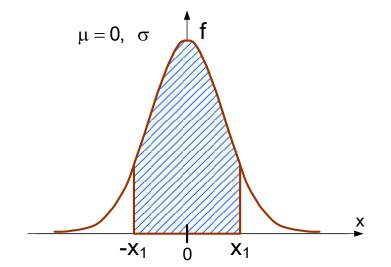
$$p(-x_{1} < x < x_{1}) = \int_{-x_{1}}^{x_{1}} f(x) dx = \int_{-x_{1n}}^{x_{1n}} f_{n}(x) dx = F_{n}(x_{1n}) - F_{n}(-x_{1n})$$

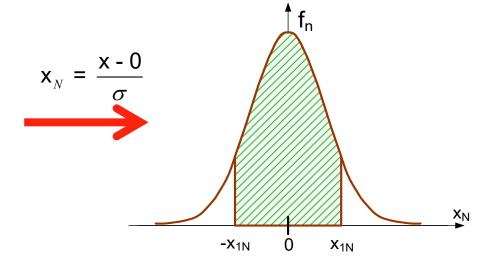
But for the N(0,1) distribution

$$F_{n}(-x_{1n}) = 1 - F_{n}(x_{1n})$$

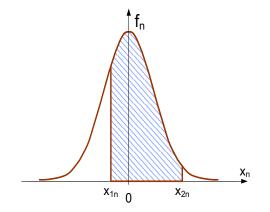
therefore:

$$p = 2F_n(x_{1n}) - 1$$

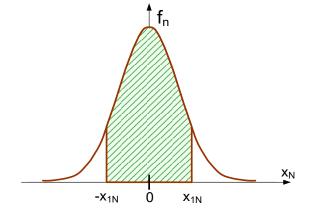




$$p = F_n(x_{2n}) - F_n(x_{1n})$$

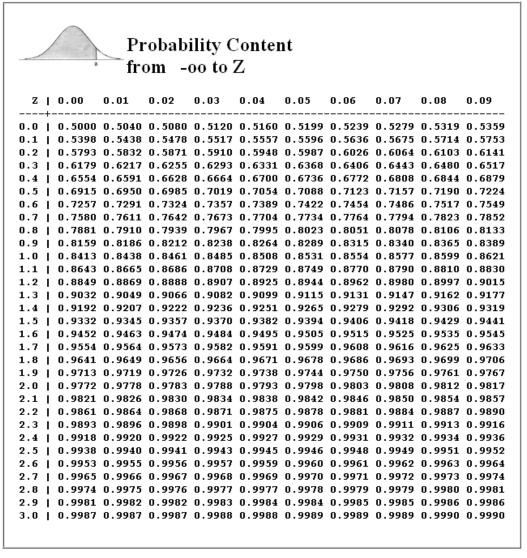


$$p = 2F_n(x_{1n}) - 1$$



Regardless of whether Gaussian performance requirements are asymmetric or symmetric, the CDF of the N(0,1) distribution (i.e. $F_n(x_n)$) can be used to characterize yield

Tables of the CDF of the N(0,1) random variable are readily available. It is also available in Matlab, Excel, and a host of other sources.



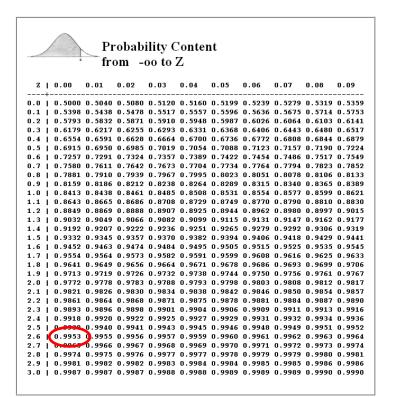
Tables of the CDF of the N(0,1) random variable are readily available. It is also available in Matlab, Excel, and a host of other sources.

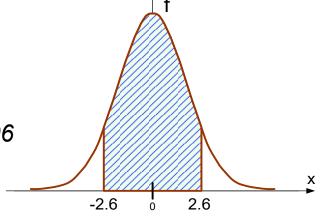
Far Right Tail Probabilities										
Z P{Z to o	00}	z	P{Z to	00}	I +	z	P{Z to oo}	l	z	P{Z to oo}
2.0 0.0227	 5	3.0	0.0013	50	T -	4.0	0.00003167	I	5.0	2.867 E-7
2.1 0.01786	5 1	3.1	0.00096	576	ı	4.1	0.00002066	ı	5.5	1.899 E-8
2.2 0.01390) [3.2	0.00068	371	ı	4.2	0.00001335	ı	6.0	9.866 E-10
2.3 0.01072	2	3.3	0.00048	334	ı	4.3	0.00000854	ı	6.5	4.016 E-11
2.4 0.00820) [3.4	0.00033	369	ı	4.4	0.000005413	ı	7.0	1.280 E-12
2.5 0.00621		3.5	0.00023	326	ı	4.5	0.000003398	ı	7.5	3.191 E-14
2.6 0.00466	51	3.6	0.00015	591	ı	4.6	0.000002112	ı	8.0	6.221 E-16
2.7 0.00346	7	3.7	0.00010	178	ı	4.7	0.000001300	ı	8.5	9.480 E-18
2.8 0.0025	55 J	3.8	0.00007	7235	ı	4.8	7.933 E-7	ı	9.0	1.129 E-19
2.9 0.00186	66	3.9	0.00004	1810	I	4.9	4.792 E-7	I	9.5	1.049 E-21

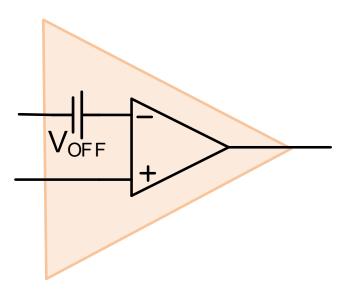
Example: Determine the probability that the N(0,1) random variable has magnitude less than 2.6

$$p = 2F_n(2.6) - 1$$

From the table of the CDF, $F_n(2.6) = 0.9953$ so p=.9906

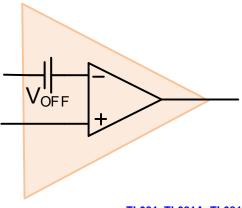






- Offset voltage of op amp can be modeled as a dc voltage source in series with input
- Offset voltage is a random variable usually zero mean and Gaussian
- Often characterized by its standard deviation
- Designer has control of offset through architecture and device sizing
- Invariably low offset voltages require large area

Offset Voltage:





TL081, TL081A, TL081B, TL081H TL082, TL082A, TL082B, TL082H TL084, TL084A, TL084B, TL084H SLOS081N – FEBRUARY 1977 – REVISED JUNE 2024

TL08xx FET-Input Operational Amplifiers

1 Features



High slew rate: 20V/µs (TL08xH, typ) Low offset voltage: 1mV (TL08xH, typ)

Low offset voltage drift: 2 μV/°C

- Low power consumption: 940µA/ch (TL08xH, typ)
- Wide common-mode and differential voltage ranges
 - Common-mode input voltage range includes V_{CC+}
- Low input bias and offset currents
- · Low noise:

 $V_n = 18nV/\sqrt{Hz}$ (typ) at f = 1kHz

- Output short-circuit protection
- Low total harmonic distortion: 0.003% (typ)
- Wide supply voltage: ±2.25V to ±20V, 4.5V to 40V

3 Description

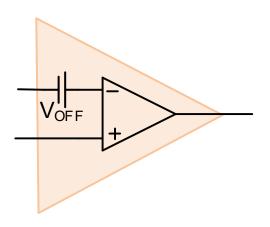
The TL08xH (TL081H, TL082H, and TL084H) family of devices are the next-generation versions of the industry-standard TL08x (TL081, TL082, and TL084) devices. These devices provide outstanding value for cost-sensitive applications, with features including low offset (1mV, typical), high slew rate (20V/µs), and common-mode input to the positive supply. High ESD (1.5kV, HBM), integrated EMI and RF filters, and operation across the full –40°C to 125°C enable the TL08xH devices to be used in the most rugged and demanding applications.

Device Information

PART NUMBER	PACKAGE ⁽¹⁾	BODY SIZE (NOM)(2)			
	P (PDIP, 8)	9.59mm × 6.35mm			
	DOK (0070 F)	0			

But read the fine print!

Offset Voltage:



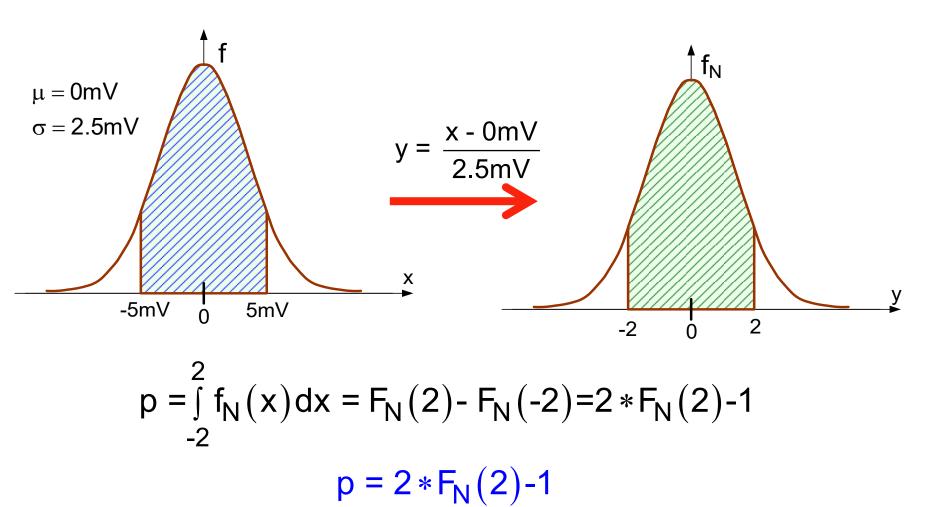
5.8 Electrical Characteristics (DC): TL08xC, TL08xAC, TL08xBC, TL08xI, TL08xM

For $V_S = (V_{CC+}) - (V_{CC-}) = \pm 15V$ at $T_A = 25$ °C, unless otherwise noted

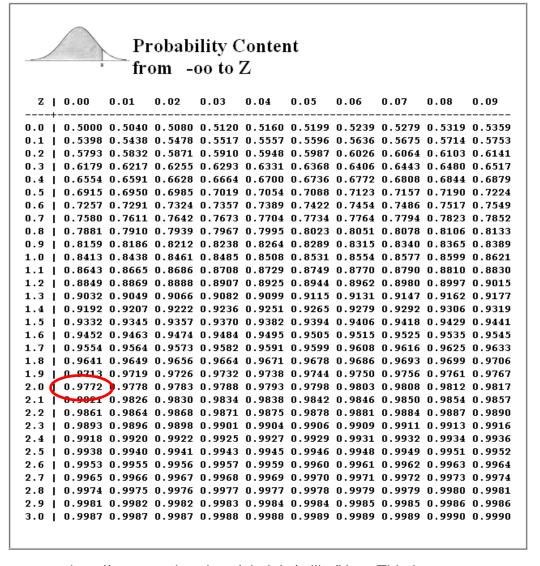
PARAMETER		TEST CONDITIONS ⁽¹⁾ (2)			MIN	TYP	MAX	UNIT
			TL08xC			3	10	
				T _A = Full range			13	
			TL08xAC			3	6	
				T _A = Full range			7.5	
			TI 00vPC			2	3	
V _{OS} Input offs	lanut effect voltage	$V_O = 0V$ $R_S = 50 \Omega$	TL08xBC	T _A = Full range			5	
	Input offset voltage		TL08xI			3	6	
				T _A = Full range			8	
			TL081M, TL082M			3	6	
				T _A = Full range			9	
			TL084M			3	9	
				T _A = Full range			15	
	land offer at welling							

At manufacture, V_{OFF} is a random variable and the TL081M has been sorted at test to cut off tails beyond $\pm~9\text{mV}$

Example: Determine the soft yield of an operational amplifier that has an offset voltage requirement of 5mV if the offset voltage has a Gaussian distribution with a standard deviation of 2.5mV and a mean of 0V.

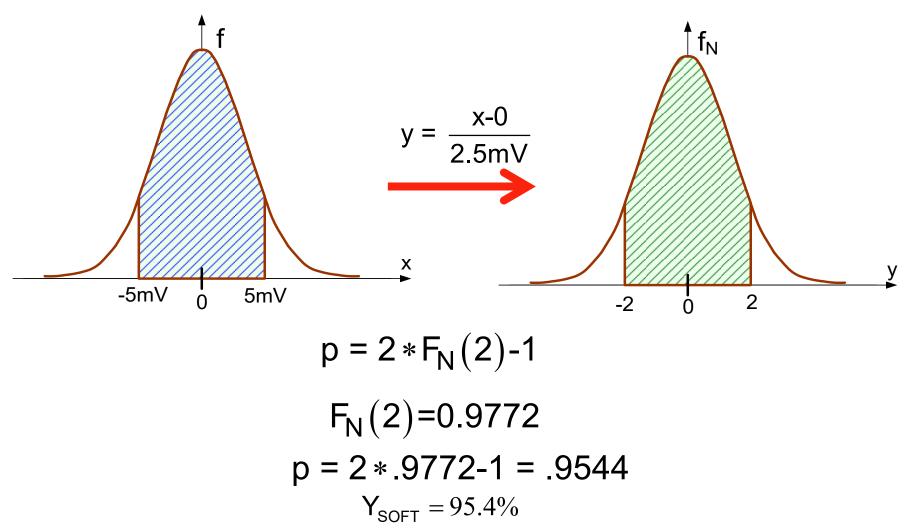


Example (continued)



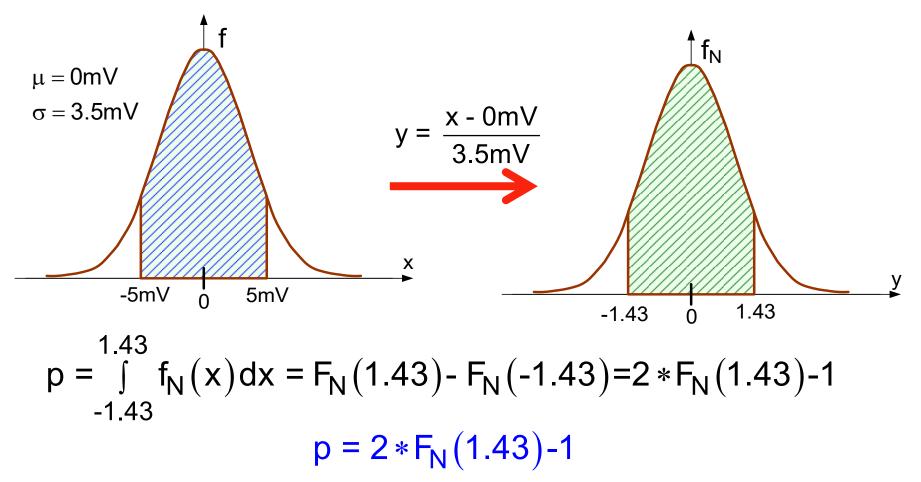
Example (continued)

Determine the soft yield of an operational amplifier that has an offset voltage requirement of 5mV if the offset voltage has a Gaussian distribution with a standard deviation of 2.5mV and a mean of 0V.

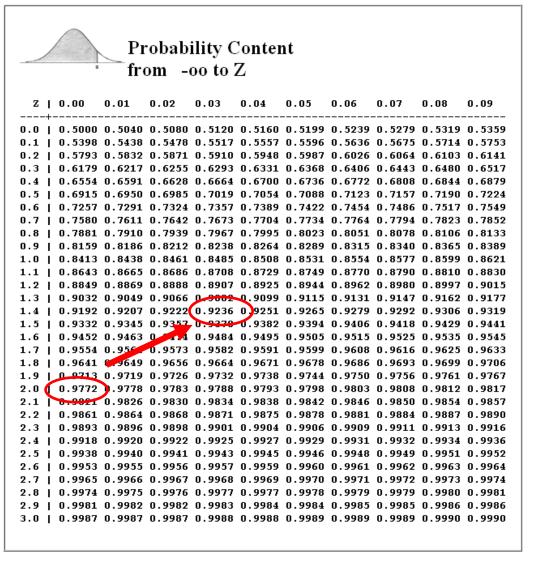


Repeat the previous example if the designer decided to reduce the area so that the standard deviation increased to 3.5 mV

Example: Determine the soft yield of an operational amplifier that has an offset voltage requirement of 5mV if the offset voltage has a Gaussian distribution with a standard deviation of 3.5mV and a mean of 0V.



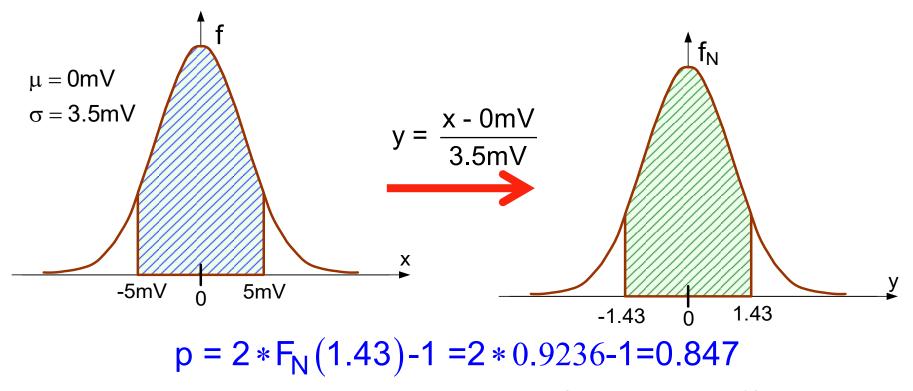
Example (continued)



Background Information

Repeat the previous example if the designer decided to reduce the area so that the standard deviation increased to 3.5 mV

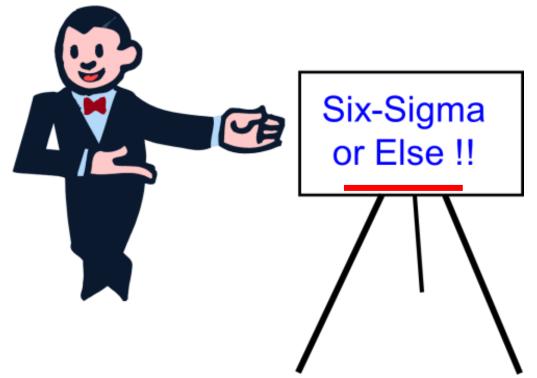
Example: Determine the soft yield of an operational amplifier that has an offset voltage requirement of 5mV if the offset voltage has a Gaussian distribution with a standard deviation of 3.5mV and a mean of 0V.



This small change in the design dropped the yield from just over 95% to just under 85%

Statistical analysis is critical for predicting performance capabilities of many ICs!

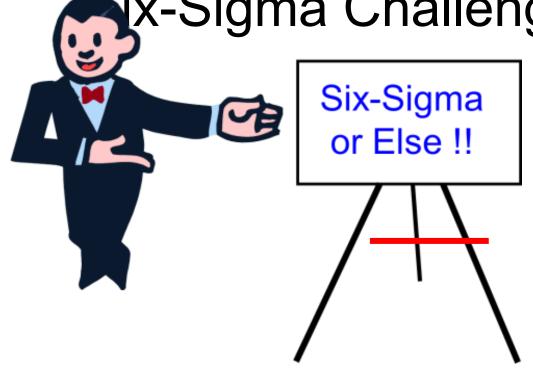
Many Companies Promote the Real Six-Sigma Challenge



From Wikipedia Sept 1 2021

Six Sigma (**6σ**) is a set of techniques and tools for process improvement. It was introduced by American engineer <u>Bill Smith</u> while working at <u>Motorola</u> in 1986. A six sigma process is one in which 99.99966% of all opportunities to produce some feature of a part are statistically expected to be free of defects.

Many Companies Promote the Real ix-Sigma Challenge



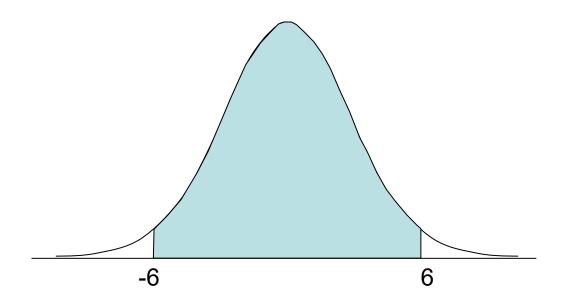
From Wikipedia Sept 1 2021

In 2005 Motorola attributed over \$17 billion in savings to Six Sigma. [3]

By the late 1990s, about two-thirds of the <u>Fortune 500</u> organizations had begun Six Sigma initiatives with the aim of reducing costs and improving quality. [6]

Yield at the Six-Sigma level

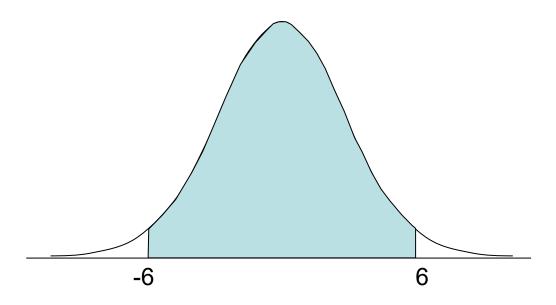
(Assume a Gaussian distribution)



$$Y_{\text{6sigma}} = 2F_N(6) - 1$$

This is approximately 2 defects out of 1 billion parts

Yield at the Six-Sigma level



This is approximately 2 defects out of 1 billion parts

Would producing ICs with a yield at the six-sigma level be a good goal?

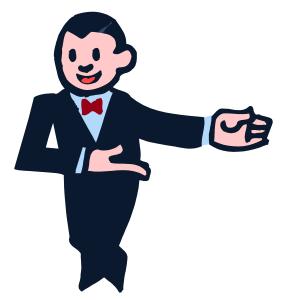
How about smart phones with defects at this level? (approx. 1.4B sold in 2020)

How about automobiles? (approx. 78 million produced in 2020)

Six-Sigma or Else!!

How serious is the "or Else" in the six-sigma programs?





Six-Sigma or Else !!

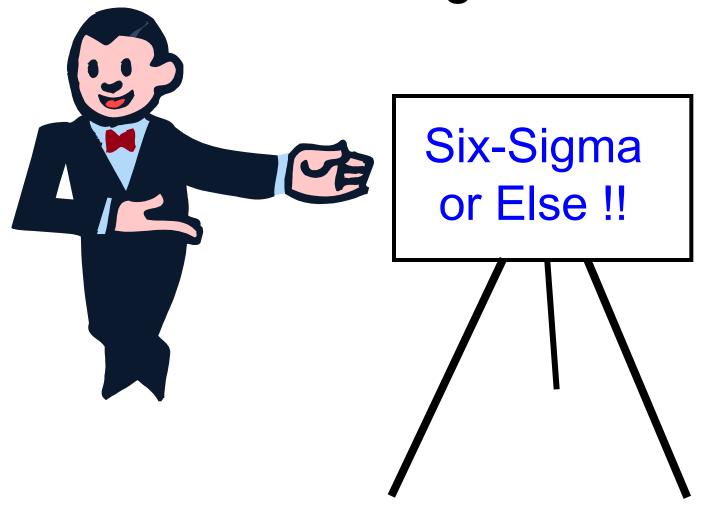
It is assumed that the performance or yield will drop, <u>for some reason</u>, by 1.5 sigma after a process has been established

Initial "six-sigma" solutions really expect only 4.5 sigma performance in steady-state production

Assumption: Processes of interest are Gaussian (Normal)

4.5 sigma performance corresponds to 3.4 defects in a million

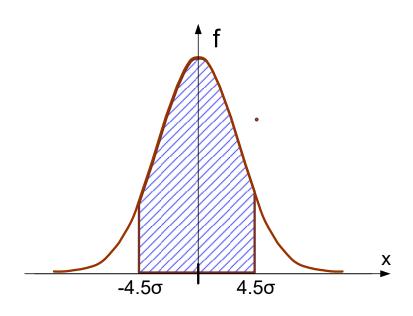
Observation: Any Normally distributed random variable can be mapped to a N(0,1) random variable by subtracting the mean and dividing by the variance

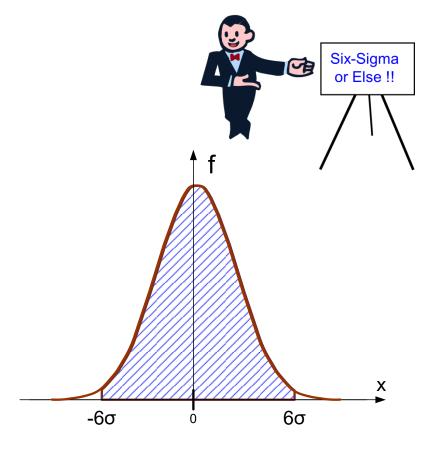


Highly Statistical Concept!

The Six-Sigma Challenge

Two-sided capability:





Long-term Capability

Tails are 6.8 parts in a million

Short-term Capability

Tail is 2 parts in a billion

Six Sigma Performance is Very Good !!!

Example: Determine the maximum die area if the circuit yield is to initially meet the "six sigma" challenge for hard yield defects (Assume a defect density of 1cm⁻² and only hard yield loss). Is it realistic to set six-sigma die yield expectations on the design and process engineers?

Solution:

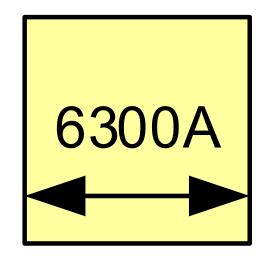
The "six-sigma" challenge requires meeting a 6 standard deviation yield with a Normal (0,1) distribution

$$Y_{6\text{sigma}} = 2F_N(6) - 1$$

Recall: $F_N(6) = 0.9999999980$

$$Y_{6 \text{sigma}} = 0.999999996$$

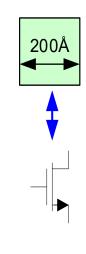
Solution cont:



Consider a 20nm process with 10x area overhead

$$A_{TRAN} = 10 * (200)^{2} (\stackrel{\circ}{A})^{2} = 4E5 (\stackrel{\circ}{A})^{2}$$

$$n = \frac{40E6(\stackrel{\circ}{A})^{2}}{4E5(\stackrel{\circ}{A})^{2}} = 100$$



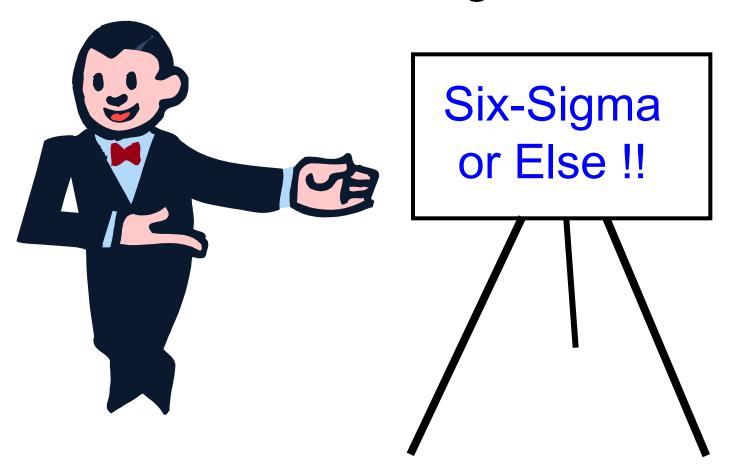
This is comparable to the area required to fabricate about 100 minimumsized transistors in a state of the art 20nm process

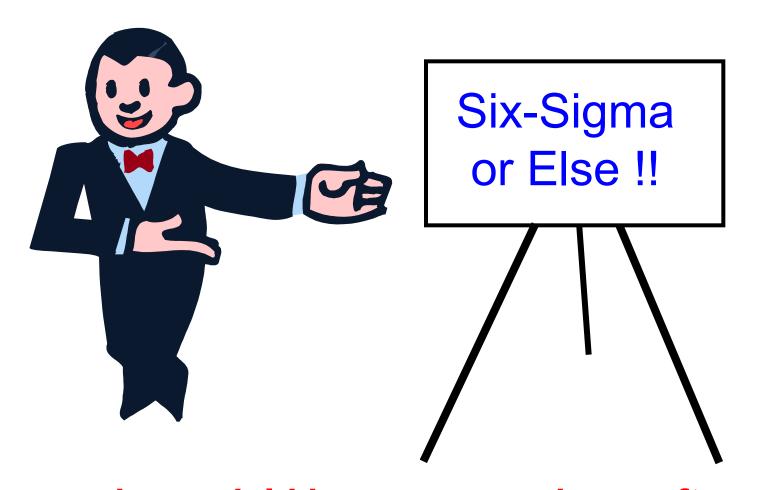
Solution cont:

Is it realistic to set six-sigma die hard yield expectations on the design and process engineers?

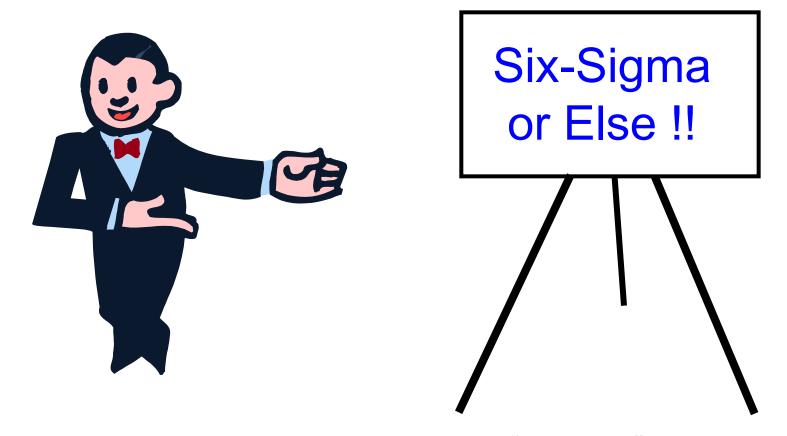
The best technologies in the world have orders of magnitude too many defects to build any useful integrated circuits with die yields that meet six-sigma performance requirements!!

Arbitrarily setting six-sigma design requirements will guarantee financial disaster !!





Improving a yield by even one sigma often is VERY challenging!!



So, how has Motorola prospered with "meeting" the 6-sigma challenge?





Six-Sigma or Else!!

How has Motorola fared with the 6-sigma approach?

Motorola, Inc. (pronounced) was an American <u>multinational</u>⁶ telecommunications company based in <u>Schaumburg, Illinois</u>, which was eventually divided into two independent public companies, <u>Motorola Mobility</u> and <u>Motorola Solutions</u> on January 4, 2011, after losing \$4.3 billion from 2007 to 2009.

How has Motorola fared with the 6-sigma approach?

- Late 90's major competitor of Intel on microprocessors
- World leader in cell phones for a number of years
- Peaked at 150,000 employees
-



- Sold military activities to General Dynamics 2000/2001
- Sold automotive products in 2006
- Spun off discrete components as ON semiconductor in 1999
- Spun off SPS as Freescale in 2003 (acquired by NXP in 2015)
- Sold Motorola Mobility to Google in 2011 (acquired by Lenovo in 2014)
- Motorola Solutions has 10,000 employees, down from over 150,000 at peak

Example: This was part of an article that appeared on Jan 26, 2022. The content of the article is not relevant but rather it serves as an example of use of statistics in our society

The Washington Post in 2018 reported the statistical likelihood of a public school student in the US being killed by gunfire at school was roughly 1 in 614,000,000 since 1999.

Is this an abuse of statistics?

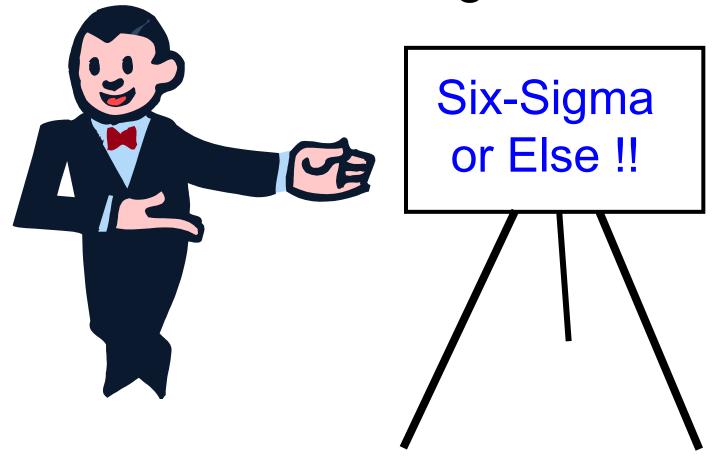
Statistics can be abused!

Many that are not knowledgeable incorrectly use statistics

Many use statistics to intentionally mislead the public

Some openly abuse statistics for financial gain or for manipulation purposes

Keep an open mind to separate "good" statistics from "abused" statistics



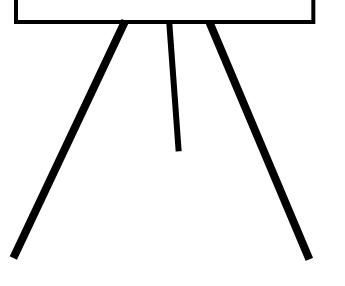
Six-sigma capability has almost nothing to do with optimizing profits and, if taken seriously, will likely **guarantee a financial fiasco** in most manufacturing processes

Meeting the real Six-Sigma

Challenge

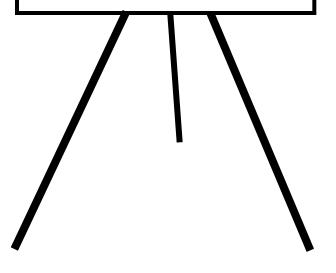
Actually optimizing a process to six-sigma performance will almost always guarantee financial disaster!

Six-Sigma or Else!!



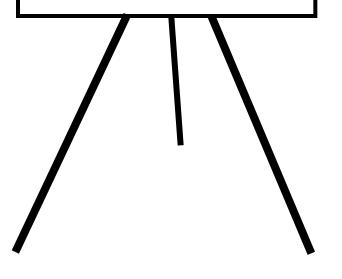


Six-Sigma or Else!!



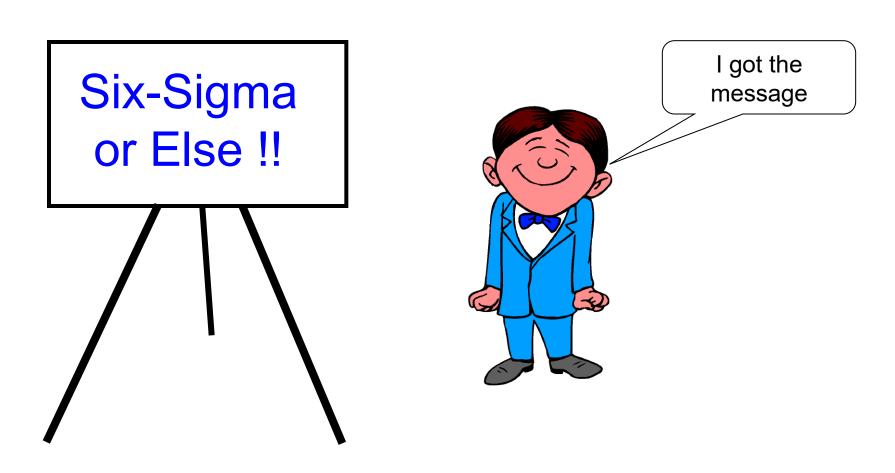


Six-Sigma or Else!!



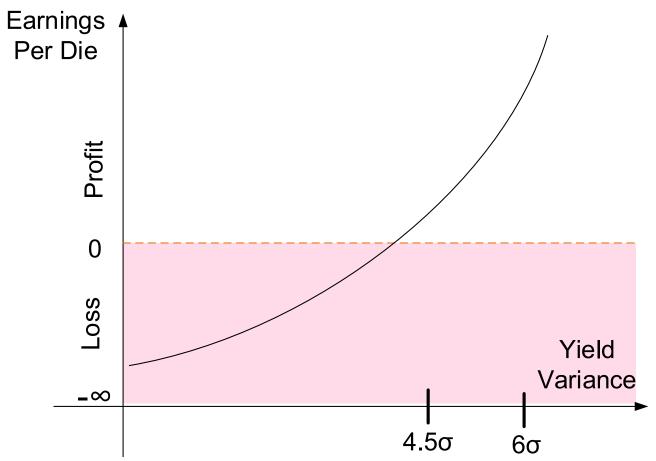


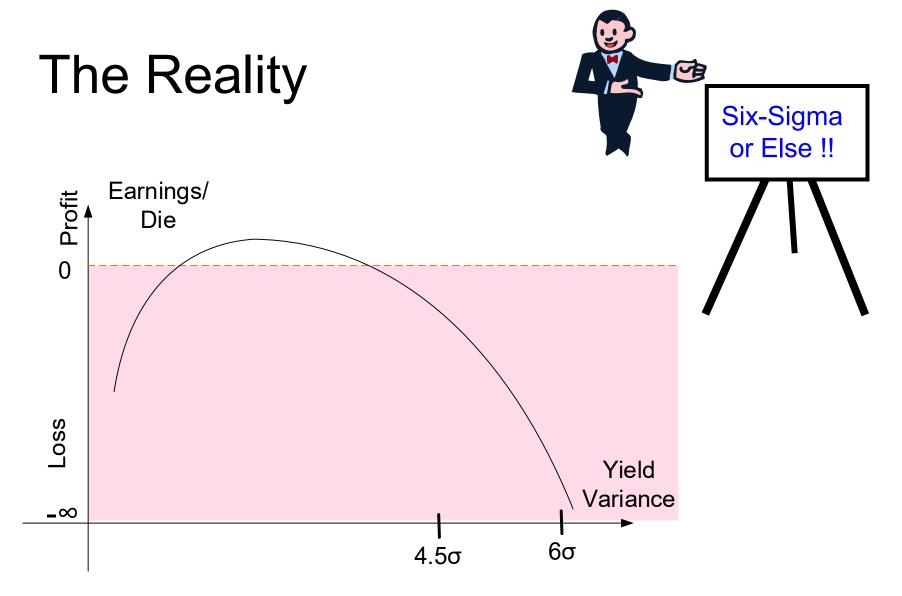
The concept of improving reliability (really profitability) is good – its just the statistics that are abused!



The Perception

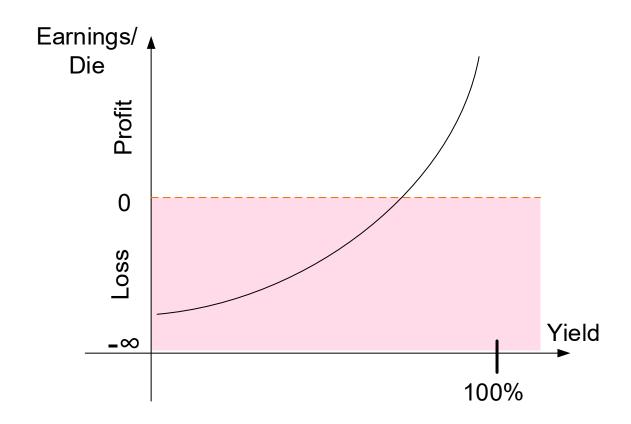






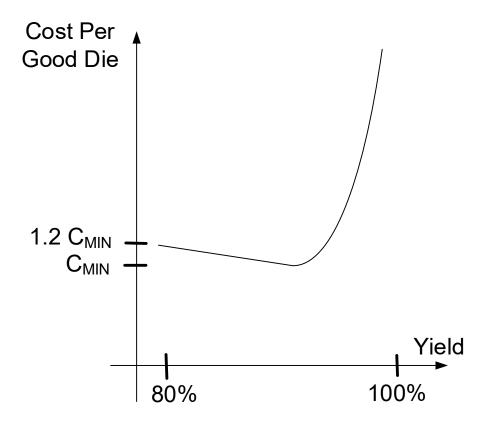
- Designing for 4.5σ or 6σ yield variance will almost always guarantee large losses
- Yield targets should be established to optimize earnings not yield variance

The Perception on Yield



Perception is often that goal should be to get yields as close to 100% as possible

The Reality about Yield



- Return on improving yield when yield is above 95% is small
- Inflection point could be at 99% or higher for some designs but below 50% for others
- Cost/good die will ultimately go to ∞ as yield approaches 100%

Designers goal should be to optimize profit, not an arbitrary yield target



Stay Safe and Stay Healthy!

End of Lecture 4